## ABSTRACT OF THE DISCLOSURE

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A discharge accelerating unit which accelerates a discharge of a capacitive load (floating capacitor C1) of a signal line when a positive polarity input gradation voltage DAC(P) decreases is provided for a positive polarity operating circuit. A charge accelerating unit which accelerates a charge of a capacitive load (floating capacitor C2) of a signal line when a negative polarity input gradation voltage DAC(N) decreases is provided for a negative polarity operating circuit. Good characteristics can be obtained irrespective of a switching period of the positive polarity operating circuit and the negative polarity operating circuit and luminance levels of adjacent dots. The number of devices which are added by the invention can be minimized.